CS-302 Quiz Preparation Virtual University

Sr	Questions	Answers Choice
1	A flip-flop is connected to +5 volts and it draws 5 mA of current during its operation, the power dissipation of the flip-flop is	A. 10 mW B. 25 mW C. 64 mW D. 1024
2	A positive edge-triggered flip-flop changes its state when _____	A. Low-to-high transition of clock B. High-to-low transition of clock C. Enable input (EN) is set D. Preset input (PRE) is set
3	Demultiplexer is also called.	A. Data selector B. Data router C. Data distributor D. Data encoder
4	An alternate method of implementing Comparators which allows the Comparators to be easily cascaded without the need for extra logic gates is _____	A. Using a single comparator B. Using Iterative Circuit based Comparators C. Connecting comparators in vertical hierarchy D. Extra logic gates are always required
5	"A + B = B + A" is _____	A. Demorgan"s Law B. Distributive Law C. Commutative Law D. Associative Law
6	The sequence of states that are implemented by a n-bit Johnson counter is	A. n+2 (n plus 2) B. 2n (n multiplied by 2) C. 2n (2 raise to power n) D. n2 (n raise to power 2)
7	Above is the circuit diagram of _____.	A. Asynchronous up-counter B. Asynchronous down-counter C. Synchronous up-counter D. Synchronous down-counter
8	_____ of a D/A converter is determined by comparing the actual output of a D/A converter with the expected output.	A. Resolution B. Missing Code C. Accuracy D. Quantization
9	FIFO is an acronym for _____	A. First In, First Out B. Fly in, Fly Out C. Fast in, Fast Out D. None of given options
10	The address from which the data is read, is provided by _____	A. Depends on circuitry B. None of given options C. RAM D. Microprocessor