

## CS-302 Quiz Preparation Virtual University

Sr	Questions	Answers Choice
1	The output of an AND gate is one when _____	A. All of the inputs are one B. Any of the input is one C. Any of the input is zero D. All the inputs are zero
2	3.3 v CMOS series is characterized by _____ and _____ as compared to the 5 v CMOS series	A. Low switching speeds, high power dissipation B. Fast switching speeds, high power dissipation C. Fast switching speeds, very low power dissipation D. Low switching speeds, very low power dissipation
3	When the control line in tri-state buffer is high the buffer operates like a _____ gate	A. AND B. OR C. NOT D. XOR
4	Generally, the Power dissipation of _____ devices remains constant throughout their operation.	A. TTL B. CMOS 3.5 series C. CMOS 5 Series D. Power dissipation of all circuits increases with time
5	$Q2 := Q1 \text{ OR } X \text{ OR } Q3$	A. $Q2 := Q1 \text{ } \$ \text{ } X \text{ } \$ \text{ } Q3$ B. $Q2 := Q1 \text{ \# } X \text{ \# } Q3$ C. $Q2 := Q1 \text{ \& } X \text{ \& } Q3$ D. $Q2 := Q1 \text{ ! } X \text{ ! } Q3$
6	The high density FLASH memory cell is implemented using _____	A. 1 floating-gate MOS transistor B. 2 floating-gate MOS transistors C. 4 floating-gate MOS transistors D. 6 floating-gate MOS transistors
7	_____ of a D/A converter is determined by comparing the actual output of a D/A converter with the expected output	A. Resolution B. Accuracy C. Quantization D. Missing Code
8	LUT is acronym for _____	A. Look Up Table B. Local User Terminal C. Least Upper Time Period D. None of given options
9	THE HOURS COUNTER IS IMPLEMENTED USING _____	A. ONLY A SINGLE MOD-12 COUNTER IS REQUIRED B. MOD-10 AND MOD-6 COUNTERS C. MOD-10 AND MOD-2 COUNTERS D. A SINGLE DECADE COUNTER AND A FLIP-FLOP
10	The design and implementation of synchronous counters start from _____	A. state table B. k-map C. state diagram D. Truth table