

CS-302 Quiz Preparation Virtual University

Sr	Questions	Answers Choice
1	In _____ outputs depend only on the combination of current state and inputs.	A. Mealy machine B. Moore Machine C. State Reduction table D. State Assignment table
2	A positive edge-triggered flip-flop changes its state when _____	A. Low-to-high transition of clock B. High-to-low transition of clock C. Enable input (EN) is set D. Preset input (PRE) is set
3	For a gated D-Latch if EN=1 and D=1 then $Q(t+1) =$ _____	A. 0 B. 1 C. $Q(t)$ D. Invalid
4	The ABEL symbol for "OR" operation is	A. ! B. & C. # D. \$
5	The capability that allows the PLDs to be programmed after they have been installed on a circuit board is called _____	A. Radiation-Erase programming method (REPM) B. In-System Programming (ISP) C. In-chip Programming (ICP) D. Electronically-Erase programming method (EEPROM)
6	Consider $A=1, B=0, C=1$. A, B and C represent the input of three bit NAND gate the output of the NAND gate will be _____	A. Zero B. One C. Undefined D. No output as input is invalid
7	NAND gate is formed by connecting _____	A. AND Gate and then NOT Gate B. NOT Gate and then AND Gate C. AND Gate and then OR Gate D. OR Gate and then AND Gate
8	The output of an XNOR gate is 1 when _____. I) All the inputs are zero II) Any of the inputs is zero III) Any of the inputs is one IV) All the inputs are one.	A. I Only B. IV Only C. I and IV only D. II and III only
9	The _____ of a ROM is the time it takes for the data to appear at the Data Output of the ROM chip after an address is applied at the address input lines.	A. Write Time B. Recycle Time C. Refresh Time D. Access Time
10	Which is not characteristic of a shift register?	A. Serial in/parallel in B. Serial in/parallel out C. Parallel in/serial out D. Parallel in/parallel out