

CS-302 Quiz Preparation Virtual University

Sr	Questions	Answers Choice
1	The simplest and most commonly used Decoders are the _____ Decoders	A. (n-1) to (2n-1) B. (n-1) to 2n C. n to 2n-1 D. n to 2n
2	The simplest and most commonly used Decoders are the _____ Decoders	A. n to 2n-1 B. n to 2n C. (n-1) to 2n D. (n-1) to (2n-1)
3	The decimal "17" in BCD will be represented as _____10001(right opt is not given)	A. 11101 B. 11101 C. 10111 D. 11110
4	The output of an XOR gate is zero (0) when _____ I) All the inputs are zero II) Any of the inputs is zero III) Any of the inputs is one IV) All the inputs are one.	A. I Only B. IV Only C. I and IV only D. II and III only
5	Caveman number system is Base _____ number system	A. 2 B. 5 C. 10 D. 16
6	$Q2 := Q1 \text{ OR } X \text{ OR } Q3$ The above ABEL expression will be	A. $Q2 := Q1 \ \$ \ X \ \$ \ Q3$ B. $Q2 := Q1 \ \# \ X \ \# \ Q3$ C. $Q2 := Q1 \ \& \ X \ \& \ Q3$ D. $Q2 := Q1 \ ! \ X \ ! \ Q3$
7	$(A + B)(A + B + C)(A + C)$ is an example of _____	A. Product of sum form B. Sum of product form C. Demorgans law D. Associative law
8	The process of converting the analogue signal into a digital representation (code) is known as _____	A. Strobing B. Amplification C. Quantization D. Digitization
9	If the FIFO Memory output is already filled with data then _____	A. It is locked; no data is allowed to enter B. It is not locked; the new data overwrites the previous data. C. Previous data is swapped out of memory and new data enters D. None of given options
10	In the following statement Z PIN 20 ISTYPE „reg.invert“; The keyword "reg.invert" indicates _____	A. An inverted register input B. An inverted register input at pin 20 C. Active-high Registered Mode output D. Active-low Registered Mode output