

CS-302 Quiz Preparation Virtual University

Sr	Questions	Answers Choice
1	The _____ input overrides the _____ input.	A. Asynchronous, synchronous B. Synchronous, asynchronous C. Preset input (PRE), Clear input (CLR) D. Clear input (CLR), Preset input (PRE)
2	_____ is said to occur when multiple internal variables change due to change in one input variable.	A. Clock Skew B. Race condition C. Hold delay D. Hold and Wait
3	74HC163 has two enable input pins which are _____ and _____.	A. ENP, ENT B. ENI, ENC C. ENP, ENC D. ENT, ENI
4	The minimum time for which the input signal has to be maintained at the input of flip-flop is called _____ of the flip-flop.	A. Set-up time B. Hold time C. Pulse Interval time D. Pulse Stability time
5	The divide-by-60 counter in digital clock is implemented by using two cascading counters:	A. Mod-6, Mod-10 B. Mod-50, Mod-10 C. Mod-10, Mod-50 D. Mod-50, Mod-6
6	In a sequential circuit the next state is determined by _____ and _____.	A. State variable, current state B. Current state, flip-flop output C. Current state and external input D. Input and clock signal applied
7	A 8-bit serial in / parallel out shift register contains the value "8", _____ clock signal(s) will be required to shift the value completely out of the register.	A. 1 B. 2 C. 4 D. 8