CS-302 Quiz Preparation Virtual University

Sr	Questions	Answers Choice
1	Given the state diagram of an up/down counter, we can find _____	A. The next state of a given present state B. The previous state of a given present state C. Both the next and previous states of a given state D. The state diagram shows only the inputs/outputs of a given states
2	_____ is said to occur when multiple internal variables change due to change in one input variable	A. Clock Skew B. Race condition C. Hold delay D. Hold and Wait
3	74HC163 has two enable input pins which are _____ and _____	A. ENP, ENT B. ENI, ENC C. ENP, ENC D. ENT, ENI
4	Flip flops are also called _____	A. Bi-stable dualvibrators B. Bi-stable transformer C. Bi-stable multivibrators D. Bi-stable singlevibrators
5	The divide-by-60 counter in digital clock is implemented by using two cascading counters	A. Mod-6, Mod-10 B. Mod-50, Mod-10 C. Mod-10, Mod-50 D. Mod-50, Mod-6
6	In a sequential circuit the next state is determined by _____ and _____	A. State variable, current state B. Current state, flip-flop output C. Current state and external input D. Input and clock signal applied
7	A frequency counter _____	A. Counts pulse width B. Counts no. of clock pulses in 1 second C. Counts high and low range of given clock pulse D. None of given options
8	A positive edge-triggered flip-flop changes its state when _____.	A. Low-to-high transition of clock B. High-to-low transition of clock C. Enable input (EN) is set D. Preset input (PRE) is set
9	In order to synchronize two devices that consume and produce data at different rates, we can use _____	A. Read Only Memory B. Fist In First Out Memory C. Flash Memory D. Fast Page Access Mode Memory
10	in _____, all the columns in the same row are either read or written.	A. Sequential Access B. MOS Access C. FAST Mode Page Access D. None of given options