CS-302 Quiz Preparation Virtual University

Sr	Questions	Answers Choice
1	The basic building block for a logical circuit is _____	A. A Flip-Flop B. A Logical Gate C. An Adder D. None of given options
2	The decimal "17" in BCD will be represented as _____	A. ₁₁₁₀₁ B. 11011 C. 10111 D. 11110
3	The ANSI/IEEE Standard 754 defines a _____Single-Precision Floating Point format for binary numbers.	A. 8-bit B. 16-bit C. 32-bit D. 64-bit
4	Stack is an acronym for_____	A. FIFO memory B. LIFO memory C. Flash Memory D. Bust Flash Memory
5	The expression F=A+B+C describes the operation of three bits _____ Gate	A. OR B. AND C. NOT D. NAND
6	DRAM stands for _____	A. Dynamic RAM B. Data RAM C. Demoduler RAM D. None of given options
7	LUT is acronym for _____.	A. Look Up Table B. Local User Terminal C. Least Upper Time Period D. None of given options
8	In asynchronous transmission when the transmission line is idle, _____	A. It is set to logic low B. It is set to logic high C. Remains in previous state D. State of transmission line is not used to start transmission
9	A logic circuit with an output consists of _____	A. two AND gates, two OR gates, two inverters B. three AND gates, two OR gates, one inverter C. two AND gates, one OR gate, two inverters D. two AND gates, one OR gate
10	The _____ input overrides the _____ input	A. Asynchronous, synchronous B. Synchronous, asynchronous C. Preset input (PRE), Clear input (CLR) D. Clear input (CLR), Preset input (PRE)