ilmkidunya

CS-302 Quiz Preparation Virtual University

Sr	Questions	Answers Choice
1	NOR gate is formed by connecting _____	A. OR Gate and then NOT Gate B. NOT Gate and then OR Gate C. AND Gate and then OR Gate D. OR Gate and then AND Gate
2	Excess-8 code assigns _____ to "+7"	A. 0000 B. 1001 C. 1000 D. 1111
3	Smallest unit of binary data is a _____	A. Bit B. Nibble C. Byte D. Word
4	In asynchronous transmission when the transmission line is idle, _____	A. It is set to logic low B. Remains in previous state C. It is set to logic high D. State of transmission line is not used to start transmission
5	In _____ outputs depend only on the current state	A. Mealy machine B. Moore Machine C. State Reduction table D. State Assignment table
6	A transparent mode means _____.	A. The changes in the data at the inputs of the latch are seen at the output B. The changes in the data at the inputs of the latch are not seen at the output C. Propagation Delay is zero (Output is immediately changed when clock signal is applied) D. Input Hold time is zero (no need to maintain input after clock transition)
7	RCO Stands for _____	A. Reconfiguration Counter Output B. Reconfiguration Clock Output C. Ripple Counter Output D. Ripple Clock Output
8	The low to high or high to low transition of the clock is considered to be a(n) _____.	A. State B. Edge C. Trigger D. One-shot
9	If the S and R inputs of the gated S-R latch are connected together using a _____gate then there is only a single input to the latch. The input is represented by D instead of S or R (A gated D-Latch)	A. AND B. OR C. NOT D. XOR
10	The _____ Encoder is used as a keypad encoder.	A. 2-to-8 encoder B. 4-to-16 encoder C. BCD-to-Decimal D. Decimal-to-BCD Priority