Computer Science 9th Class English Medium Chapter 3 Online Test

Sr	Questions	Answers Choice
1	What is the decmimal equivalent of the binary number 1101?	A. 13 B. 11 C. 12 D. 14
2	In a half-adder circuit, the carry is generated by which operation?	A. XOR operation B. AND Operation C. OR Operation D. NOT Operation
3	Which logic gate outputs true only if both inputs are true?	A. OR gate B. AND gate C. XOR gate D. NOT gat
4	What is the dual of Booean expression A.0=0?	A. A+1=1 B. A + 0 = A C. A. 1 = A D. A.O = 0
5	Which of the following Boolean expressions represents the OR operation	A. A.B B. A+B C. A D. A-B
6	In K-map , K stand for.................?	A. Knowldege B. Karnaugh C. Both a and b D. None of these
7	Comparison of two or more values are performed in?	A. CU B. UDP C. LU D. None of these
8	Basic Arthemetic Operation like addition subtractio are performed by?	A. ALU B. CU C. UDP D. All of these
9	CU stand for...............	A. Circuitry Unit B. Central Unit C. Control Unit D. None of these
10	ALU Stands for..........	A. Arithmetic and Local Unit B. Arithmetic and Legal Unit C. Arithmetic and Logic Unit D. None of these
11	OR operation yields 1 output when at least of........of the input is true.	A. 0 B. 00 C. 1 D. 10
12	the output of AND Operation is 1 only whenboth inputs are?	A. 10 B. 0 C. 1 D. 00