

Computer Science 9th Class English Medium Chapter 3 Online Test

Sr	Questions	Answers Choice
1	Digital system depends on?	A. 10 B. 00 C. 11 D. None of these
2	In digit system 0 epresents?	A. OF B. OFF C. ON D. All of these
3	In digit system 1 represents?	A. ON B. OFF C. OF D. All of these
4	Bit stand for?	A. Binary integer B. Binary Value C. Binary Number D. Binary digit
5	What does ADC represents?	A. Analog Digital Conversation B. Analog Digital Conversion C. Analog Digital Convenient D. All of these
6	What does DAC represents?	A. Analog Digital Convenient B. Analog Digital Conversation C. Digitla analog conversion D. All of these
7	Whihc signals are in discree form?	A. Analog B. Digital C. Both a and b D. None of these
8	Which signals are in continues form?	A. Digital B. Analog C. Both a and b D. None of these
9	Which one is the example of Analog signal?	A. Binary B. Sound Wave C. 10 D. None of these
10	Whcih one of the example of Digital Signal?	A. Binary B. Sound Wave C. 11 D. 00
11	the output of AND Operation is 1 only whenboth inputs are?	A. 10 B. 0 C. 1 D. 00
12	OR operation yields 1 output when at least ofof the input is true.	A. 0 B. 00 C. 1 D. 10
13	ALU Stands for	A. Arithmetic and Local Unit B. Arithmetic and Legal Unit C. Arithmetic and Logic Unit D. None of these
14	CU stand for	A. Circuitry Unit B. Central Unit C. Control Unit D. None of these
15	Basic Arthemetic Operation like addition subtractio are performed by?	A. ALU B. CU C. UDP D. All of these

A CU B. UDP C. LU D. None of these A Knowldege B. Karnaugh C. Both a and b D. None of these Which of the following Boolean expressions represents the OR operation What is the dual of Booean expression A.0=0? Which logic gate outputs true only if both inputs are true? A ACU B. UDP C. LU D. None of these A Knowldege B. Karnaugh C. Both a and b D. None of these A AB B. A+B C. A D. A-B A A+1=1 B. A + 0 = A C. A. 1 = A D. A.O = 0 A OR gate B. AND gate C. XOR gate D. NOT gat In a half-adder circuit, the carry is generated by which operation? A XOR operation D. NOT Operation			
17 In K-map , K stand for? 18 Which of the following Boolean expressions represents the OR operation 19 What is the dual of Booean expression A.0=0? 20 Which logic gate outputs true only if both inputs are true? 21 In a half-adder circuit, the carry is generated by which operation? 22 What is the decmimal equivalent of the binary number 1101? 23 B. Karnaugh C. Both a and b D. None of these D. None of these D. Not gate D. A and D. A because D. Not gate D. Not	16	Comparison of two or more values are performed in?	B. UDP C. LU
Which of the following Boolean expressions represents the OR operation B. A+B C. A D. A-B What is the dual of Booean expression A.0=0? Which logic gate outputs true only if both inputs are true? A. A+1=1 B. A + 0 = A C. A. 1 = A D. A.O = 0 A. OR gate B. AND gate C. XOR gate D. NOT gat In a half-adder circuit, the carry is generated by which operation? A. XOR operation C. OR Operation D. NOT Operation	17	In K-map , K stand for?	B. Karnaugh C. Both a and b
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21 In a half-adder circuit, the carry is generated by which operation? B. AND Operation C. OR Operation D. NOT Operation A. 13 B. 11 C. 12	20	Which logic gate outputs true only if both inputs are true?	B. AND gate C. XOR gate
What is the decrimal equivalent of the binary number 1101? B. 11 C. 12	21	In a half-adder circuit, the carry is generated by which operation?	B. AND Operation C. OR Operation
D. 14	22	What is the decmimal equivalent of the binary number 1101?	B. 11